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PATENT
Docket No. INTEL/13440



IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE

Applicants: Edirisooriya et al.

Serial No.: 10/073,492

Filed: February 11, 2002

For: "Methods and Apparatus for Cache
Intervention"

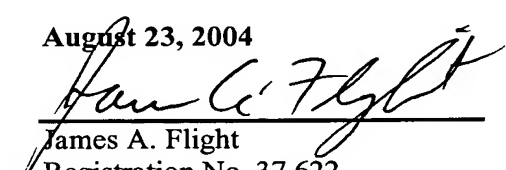
Assignee: Intel Corporation

Group Art Unit: 2187

Examiner: Brian R. Peugh

) I hereby certify that this paper is
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) Mail Stop Amendment,
) Commissioner for Patents, P.O.
) Box 1450, Alexandria, VA 22313-
) 1450 on this date:

) August 23, 2004

) 
) James A. Flight
) Registration No. 37,622
) Attorney for Applicants

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are
submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98.

08/27/2004 MBELETE1 00000041 10073492

02 FC:1806

180.00 0P

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a final action under 37 CFR §1.113 or notice of allowance under 37 CFR §1.311. Therefore, in accordance with 37 CFR §1.97(c), submitted herewith is the fee set forth in 37 CFR §1.17(p) for submission of an information disclosure statement under 37 CFR §1.97(c).

PRIOR AND RELATED APPLICATIONS

In accordance with 37 CFR §1.98(d), copies of the patents and publications listed on the enclosed PTO Form-1449 (excluding U.S. patent serial no. 6,775,748) are not provided because the patents and publications were previously cited by, or submitted to, the Patent Office in related applications. Documents listed on the attached Form PTO-1449 were cited by, or submitted to, the examiner of the following patent applications:

Applicants:	Jamil et al.
Serial No.	10/057,493 (U.S. Patent No. 6,775,748)
Filing Date:	January 24, 2002
Title:	"Methods an Apparatus for Transferring Cache Block Ownership"
Status:	Granted

Other applications related to application Serial No. are:

Applicants:	Jamil et al.
Serial No.	10/303,931
Filing Date:	November 25, 2002
Title:	"Methods and Apparatus for Cache Intervention"
Status:	Pending

METHOD OF PAYMENT

☒ Attached is a check in the amount of \$524.00 (includes \$344.00 fee for enclosed response to the Office action for this matter).

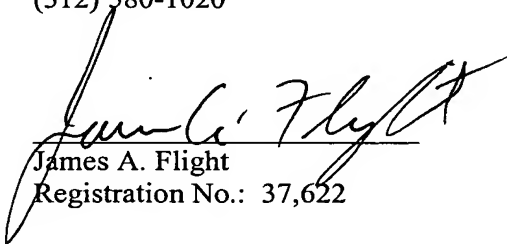
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

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By:


James A. Flight
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August 23, 2004

Attorneys for Intel Corporation

Form PTO-1449 (Modified)

U.S. Department of Commerce
Patent and Trademark Office

Atty. Docket No.

Serial No.

20002/13440

10/073,492

Applicant

Edirisooriya et al.

Filing Date

February 11,
2002

Group Art Unit

2187

SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	INVENTOR(S)	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	5,664,150	09/1997	Isaac et al.			
	A02	5,913,226	06/1999	Sato			
	A03	5,943,684	08/1999	Arimilli et al.			
	A04	6,073,211	06/2000	Cheng et al.			
	A05	6,170,040	01/2001	Lee et al.			
	A06	6,345,340	02/2002	Arimilli et al.			
	A07	6,360,301	03/2002	Gaither et al.			
	A08	6,378,048	04/2002	Prudvi et al.			
	A09	6,438,660	08/2002	Reams			
	A10	6,549,989	04/2003	Arimilli et al.			
	A11	6,549,990	04/2003	Hughes et al.			
	A12	6,574,710	06/2003	Gaither et al.			
	A13	6,615,322	09/2003	Arimilli et al.			
	A14	6,629,212	09/2003	Arimilli et al.			
	A15	6,775,748	08/2004	Jamil et al.			
	A16	2003/0154352	08/2003	Jamil et al.			

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (Modified)

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Applicant

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STATEMENT

(Use several sheets if necessary)

FOREIGN PATENT DOCUMENTS

*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation	
							Yes	No
	B01							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	Intel® Itanium™ Processor Hardware Developer's Manual, printed from ftp://download.intel.com/design/Itanium/Downloads/24870102.pdf , document no. 248701-002. (August 2001).
	C02	Intel® Itanium™ 2 Processor Hardware Developer's Manual, ftp://download.intel.com/design/Itanium2/manuals/25110901.pdf , document no. 251109-001. (July 2002).
	C03	Intel® Pentium™ 4 Processor with 512-KB L2 Cache on 0.13 Micron Process Datasheet. ftp://download.intel.com/design/Pentium4/datashts/29864309.pdf . Document no. 298643-009. (May 2003).

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